Syllabus for EE-761
EE 331/531-(3.0) – Electronic Devices

Website: http://www.cs.wright.edu/~ashaw

Fall Quarter, 2002

**INSTRUCTOR:** DR. ARNAB K. SHAH
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**Office Hrs.**: 10-10:30AM and 3:30-4PM, Monday and Wednesday and *by appointment*


**Chapters Covered:**

- **Chapter 3**: Diodes. Sections 3.1, 3.2, 3.3 (brief), 3.4-3.8
- **Chapter 5**: Field-Effect Transistors (FETs), Sections 5.1-5.6, handouts, 5.12 (if time permits)
- **Chapter 4**: Bipolar Junction Transistors (BJTs), Sections 4.1-4.11, handouts, 4.12 (if time permits)

**Grading policy:**

Mid-term I : 20 pts (Date : Oct. 16th)
Mid-term II : 20 pts (Tentative Date : Nov. 13th)
In-Class Quizzes (about six) 5 pts
Home Work (about six) 10pts
Final Exam (Comprehensive) : 45 pts (Thursday, Dec. 5th, 1:00-3:00 pm)
If the total score is less than 50%, the final grade will be *F*!!

**Other Information**

(a) No make-up examination will be given unless the student has received permission from the instructor prior to the examination or has experienced some emergency beyond control!!
(b) Students are permitted to use a self-prepared study guide (Formulas, Tables etc.) during the exams, as announced by the Instructor prior to the exams. Students are **not allowed** to bring in solved problems, steps or procedures to solve particular types of problems.
(c) The students are expected to make use of the posted office hours to have their academic difficulties resolved.
   Students are welcome at other times, but please make a call to make sure the Instructor is available.
(d) The instructor is responsible to provide only the first Mid-term exam grade before the final drop date.