

BME/ISE 3512 Bioelectronics Test Eight (Digital Electronics) Review Notes and Practice Problems

Define the terms *combinational logic* and *sequential logic*; and provide examples of each.

Define the terms *synchronous machine* and *asynchronous logic*.

Compare and contrast *synchronous* and *asynchronous* sequential logic circuits.

Describe operation of *Astable*(Free Running), *Mono-Stable*(One Shot), and *Bi-Stable*(Flip Flop) multivibrators.

Describe practical electronic examples/uses for each of the following: Schmitt Trigger, JK Flip Flop, NE 555.

Explain the *Not Allowed* States for RS Flip Flops

Draw a simple NAND gate circuit that prevents the "Not Allowed" state for a NAND RS Flip Flop.

Explain the operation of and write the Truth Table for a JK Flip Flop.

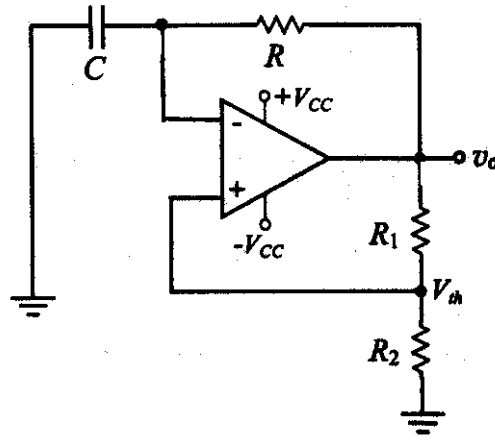
Calculate the operating frequency and period for a Schmitt Trigger Square Wave Generator (page 2).

Calculate the operating frequency, output time periods, and duty cycle of an 555 astable multivibrator (page 3).

Analyze the operation of an Open Loop Op-Amp LED Driver Circuit (page 4).

Calculate the operating frequency and cycle period for Schmitt Trigger Square Wave Oscillator.

Example: $R_1 = 4050 \Omega$, $R_2 = 3480 \Omega$, $R = 5000 \Omega$, $C = 0.01 \mu\text{F}$



Answers:

b. Calculate the Cycle Period

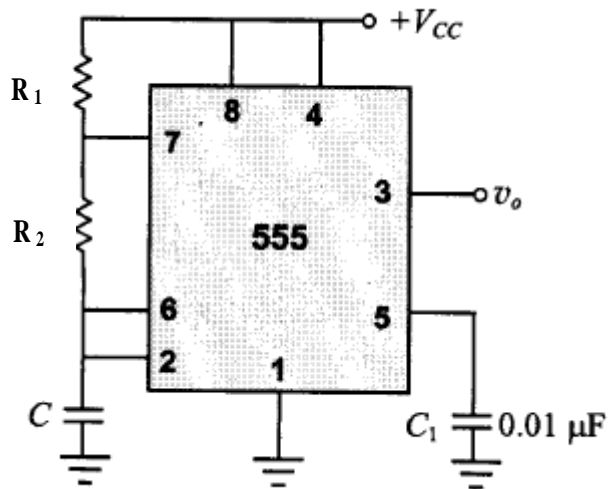
100 μsec

b. Calculate Operating Frequency

10 KHz

Calculate the operating frequency, output time periods, and duty cycle of an 555 astable multivibrator.

Example: $R_1 = 390 \text{ K}\Omega$, $R_2 = 680 \text{ K}\Omega$, and $C = 1 \text{ }\mu\text{f}$



Answers:

a. Calculate the On & Off Pulse Widths (i.e., t_H & t_L)

$$t_H = 0.74 \text{ sec} \quad t_L = 0.47 \text{ sec}$$

b. Calculate the Period

$$1.2 \text{ sec}$$

b. Calculate Duty Cycle

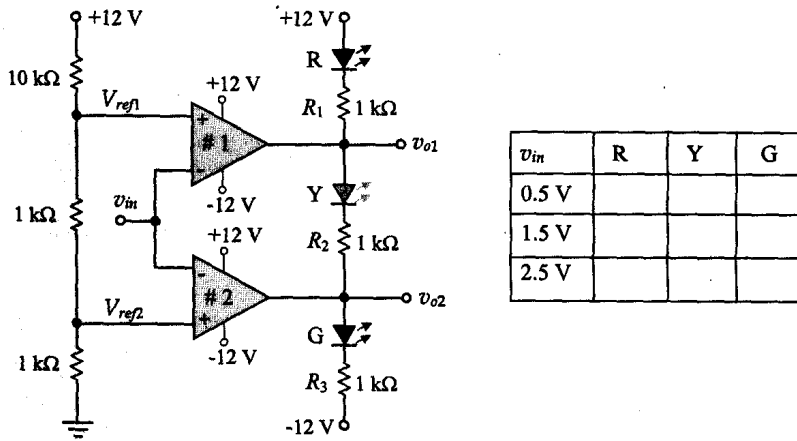
$$61 \%$$

c. Calculate the overall Operating Frequency

$$0.83 \text{ Hz}$$

For the LED Driver problem below, assume the following values: $V_{sat} = \pm 10\text{ V}$, $V_{LED} = 2.4\text{ V}$.

Note: $V_{cc} = \pm 12\text{ V}$, which also serves as the voltage divider source on the left and the LED sources on the right.



v_{in}	R	Y	G
0.5 V			
1.5 V			
2.5 V			

1. Calculate V_{Refs}

$$R_{Total} = 10K + 1K + 1K = 12\text{ K}\Omega$$

$$I = 12\text{ V} / 12\text{ K}\Omega = 1\text{ mA}$$

$$V_{Ref2} = 1\text{ mA} \times 1\text{ K}\Omega = 1\text{ V}$$

$$V_{Ref1} = 1\text{ mA} \times (1\text{ K}\Omega + 1\text{ K}\Omega) = 2\text{ V}$$

2. Determine V_{Outs}

If $V_{in} < V_{Ref2}$ Then $V_{Out2} = +V_{sat}$ that is to say if $V_{in} < 1\text{ V}$ Then $V_{Out2} = +10\text{ V}$
 If $V_{in} > V_{Ref2}$ Then $V_{Out2} = -V_{sat}$ that is to say if $V_{in} > 1\text{ V}$ Then $V_{Out2} = -10\text{ V}$
 If $V_{in} < V_{Ref1}$ Then $V_{Out1} = +V_{sat}$ that is to say if $V_{in} < 2\text{ V}$ Then $V_{Out1} = +10\text{ V}$
 If $V_{in} > V_{Ref1}$ Then $V_{Out1} = -V_{sat}$ that is to say if $V_{in} > 2\text{ V}$ Then $V_{Out1} = -10\text{ V}$

V_{in}	V_{Out1}	V_{Out2}	$+12\text{V} - V_{Out1}$ Red	$V_{Out1} - V_{Out2}$ Yellow	$V_{Out2} - (-12\text{V})$ Green
0.5 V	+10 V	+10 V	+2 V	0 V	+22 V
1.5 V	+10 V	-10 V	+2 V	+20 V	+2 V
2.5 V	-10 V	-10 V	+22 V	0 V	+2 V

3. Determine LED State: If voltage across LED and its associated 1 KΩ resistor $> 2.4\text{ V}$, then LED is ON.

V_{in}	Red	Yellow	Green
0.5 V	OFF	OFF	ON
1.5 V	OFF	ON	OFF
2.5 V	ON	OFF	OFF